



```

graph TD
    InputUnit[INPUT UNIT] --> CPU[CPU]
    CPU --> OutputUnit[OUTPUT UNIT]
    CPU --> Memory[MEMORY]
    Memory --> CPU
    
```

```

graph TD
    A[ ] --> B[GENERATION UNIT]
    style A fill:none,stroke:none
  
```



RECEIVER

FIG. 1

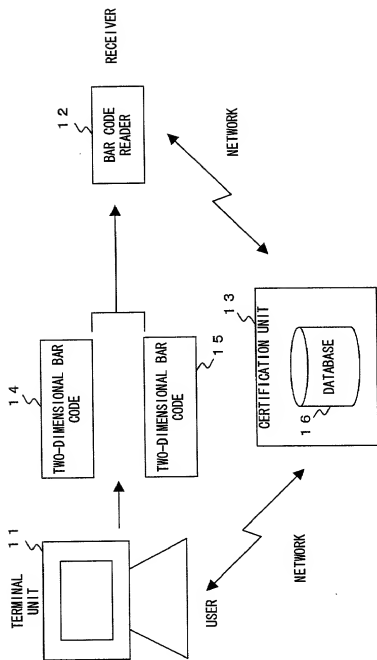


FIG. 2

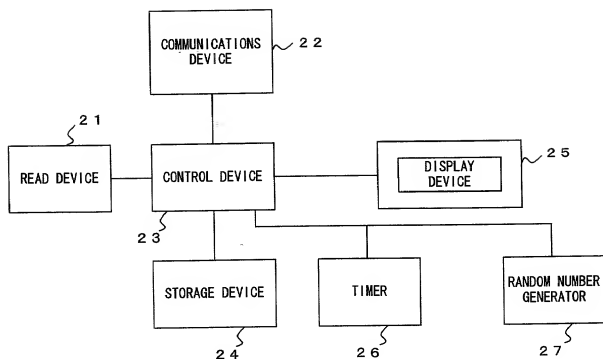


FIG. 3

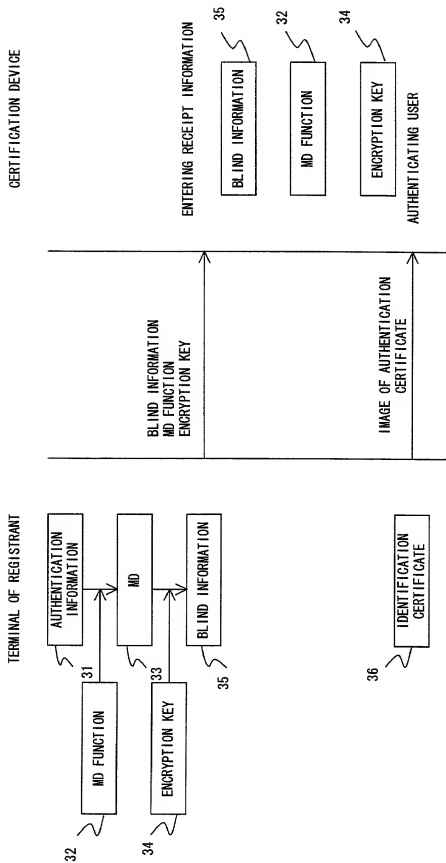


FIG. 4

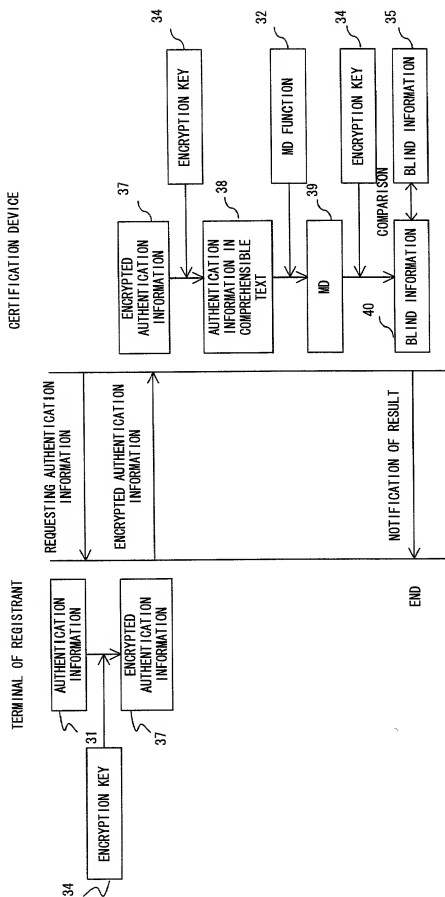


FIG. 5

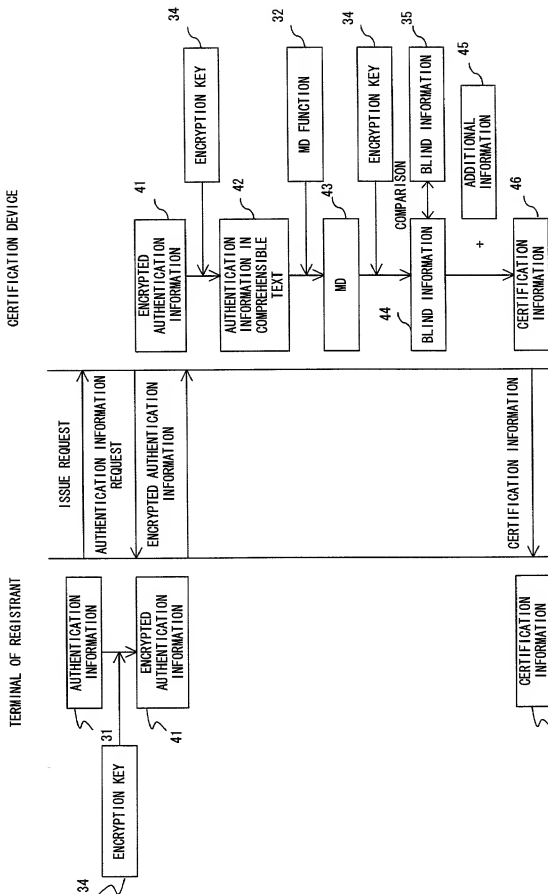


FIG. 6

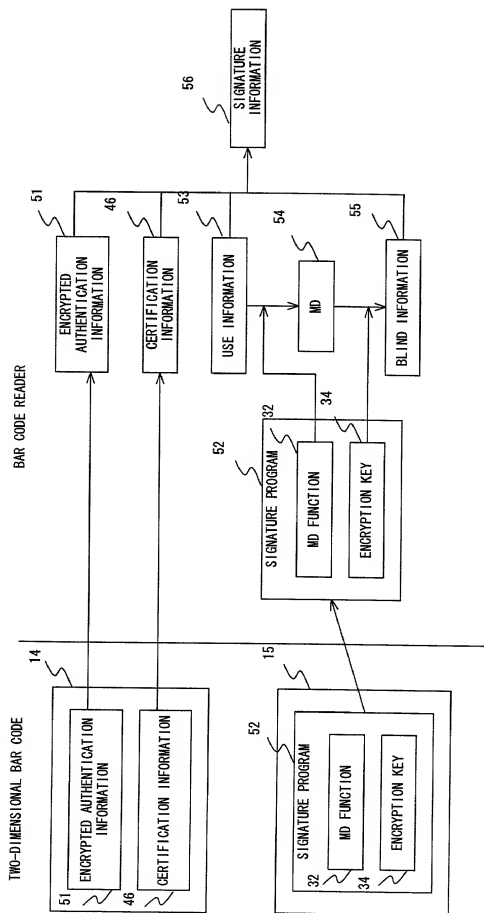


FIG. 7

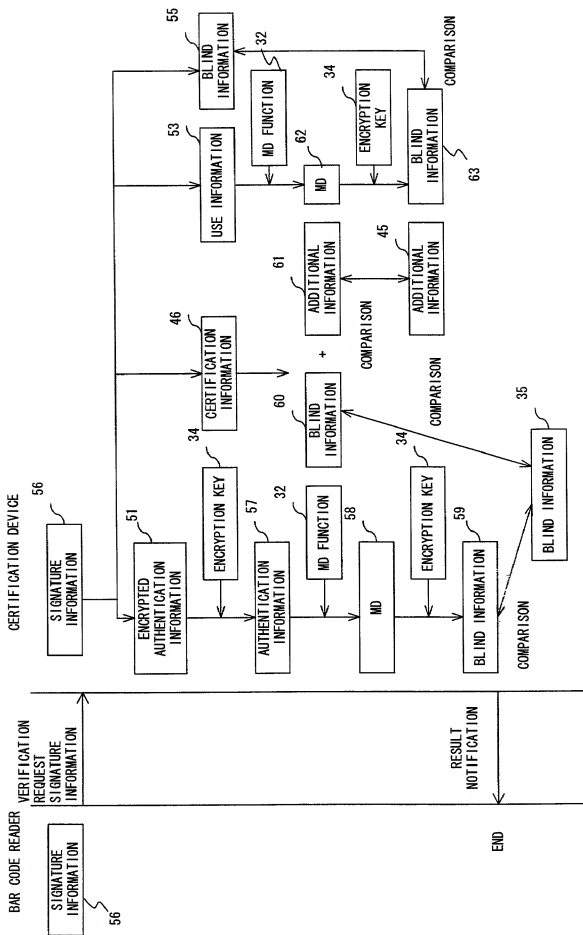


FIG. 8

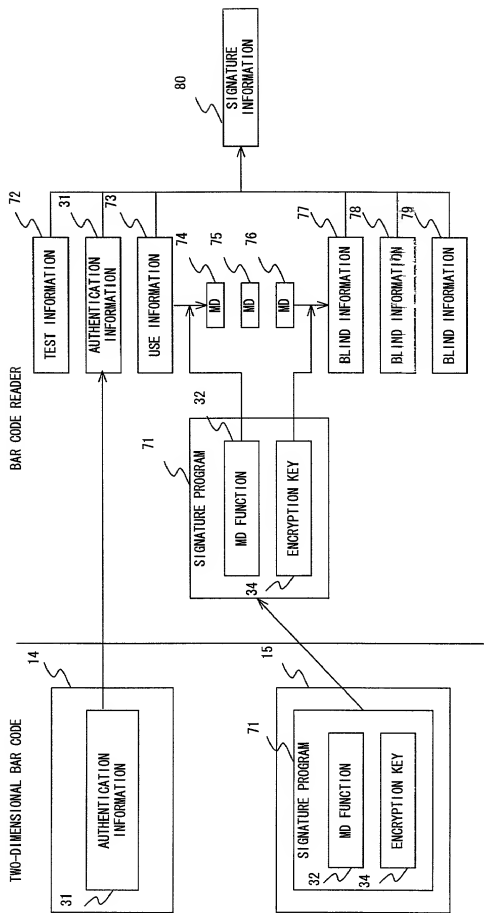


FIG. 9

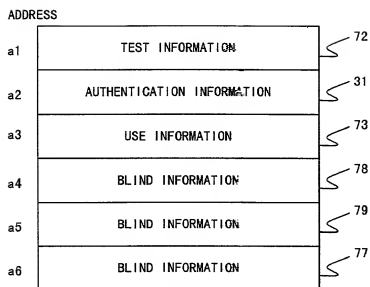


FIG. 10

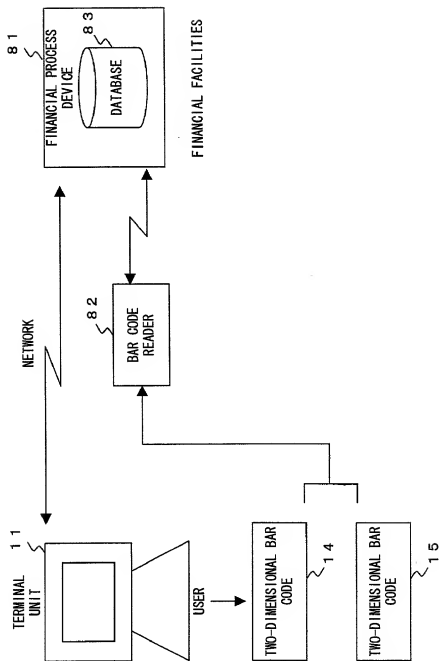


FIG. 11

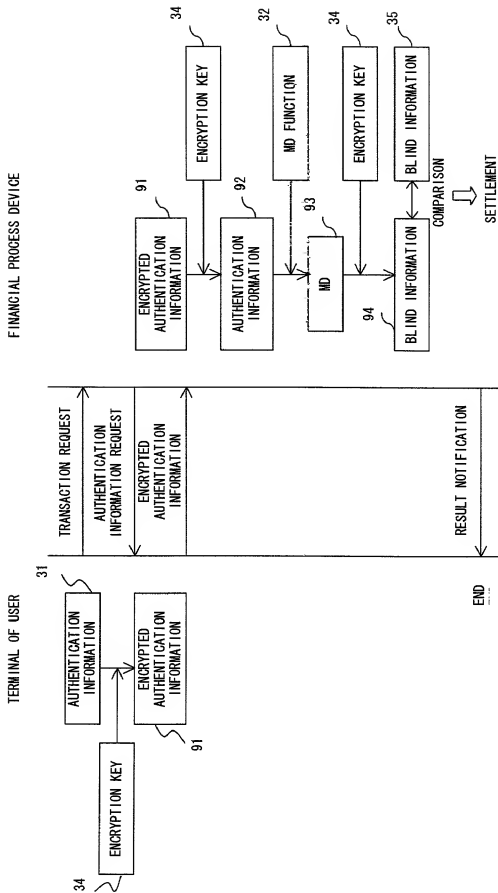


FIG. 12

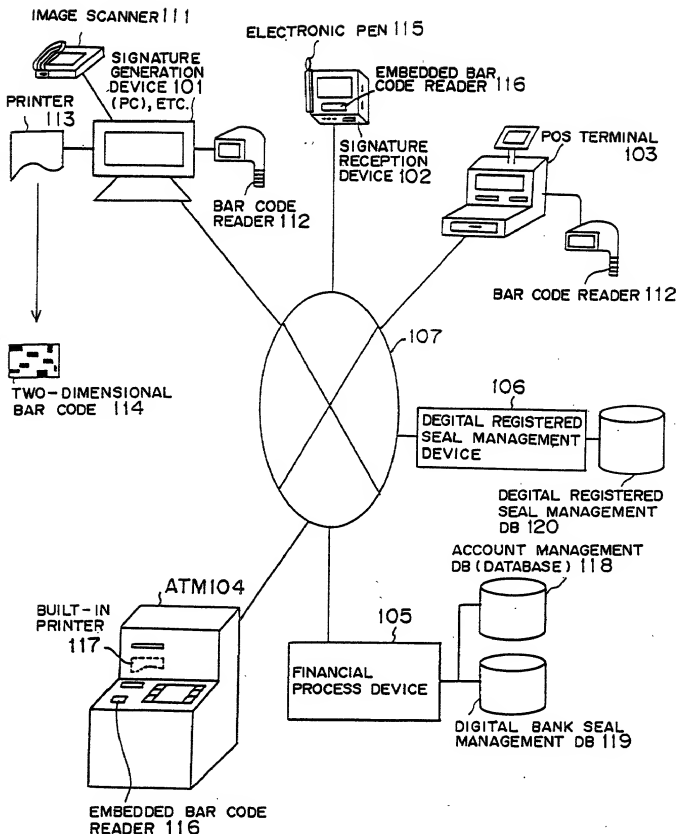


FIG. 13

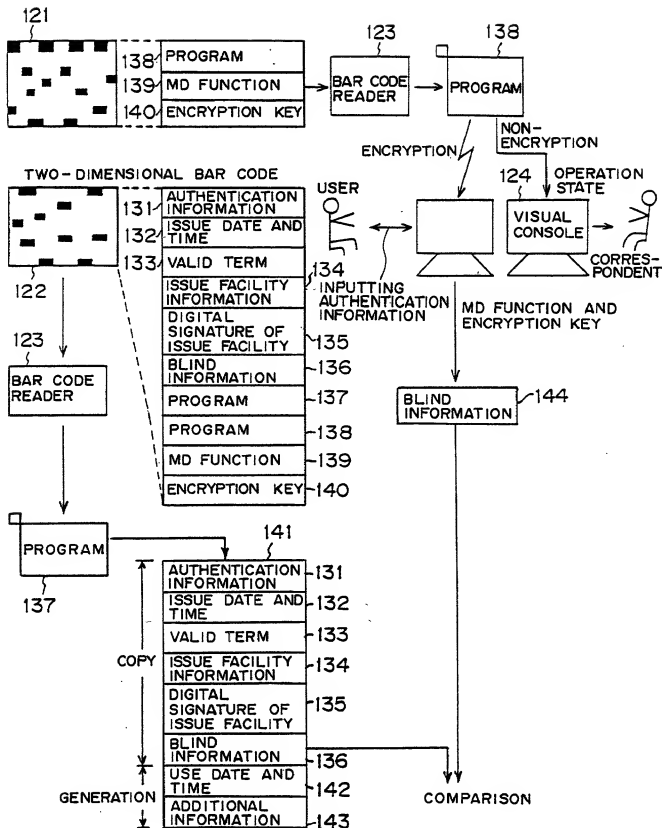


FIG. 14

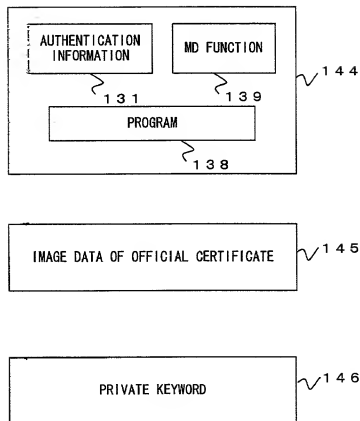


FIG. 15

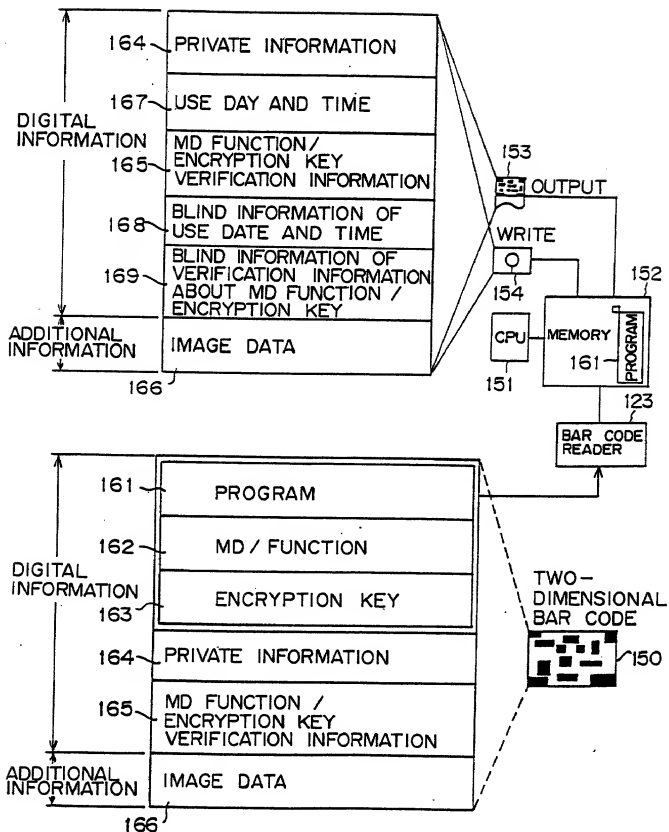


FIG. 16

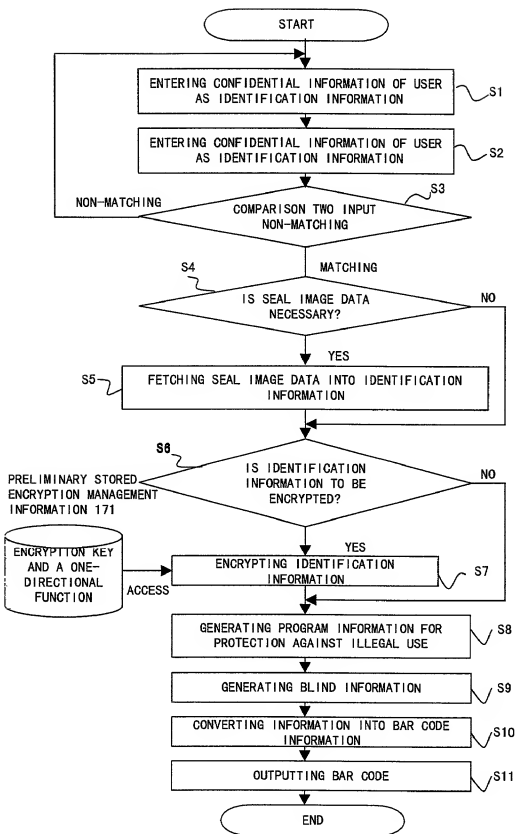


FIG. 17

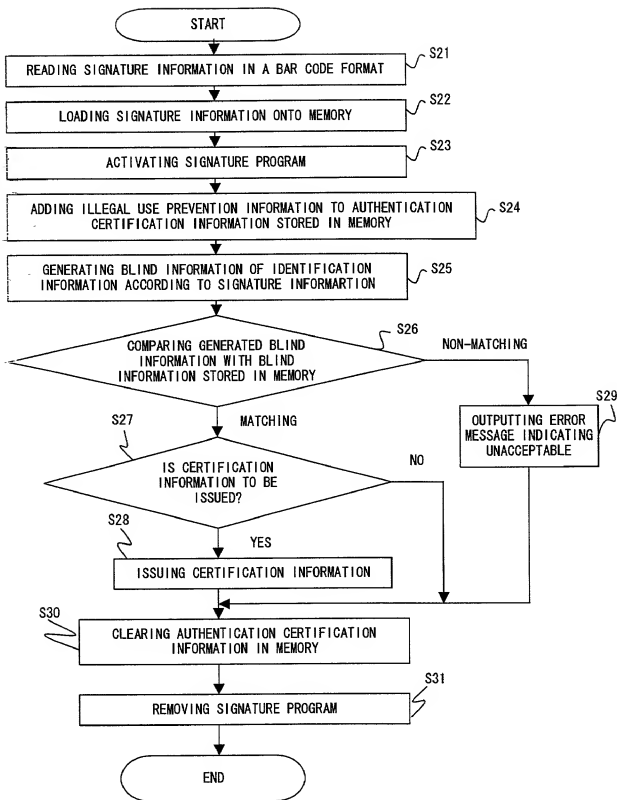
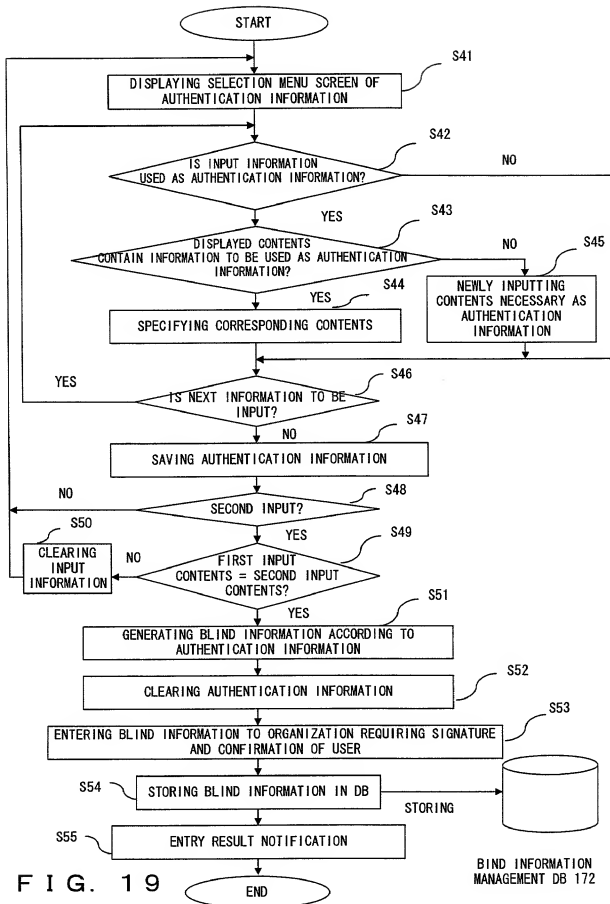


FIG. 18



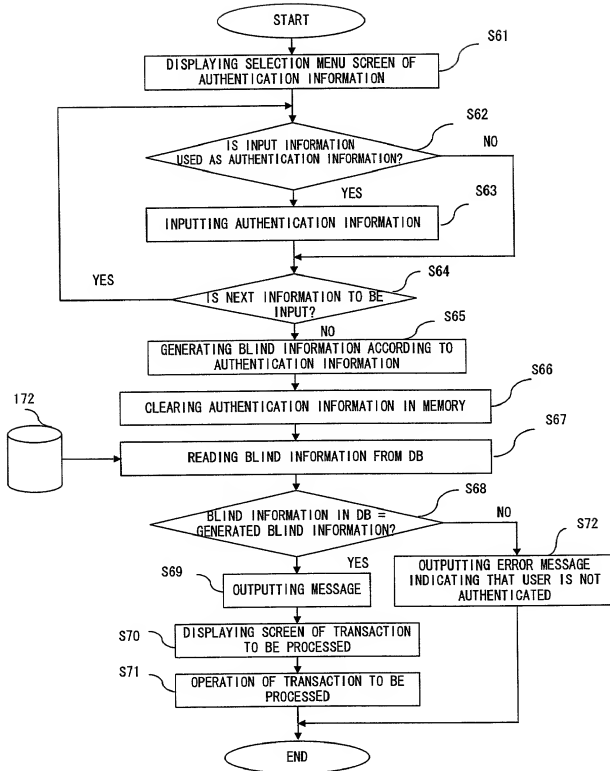


FIG. 20

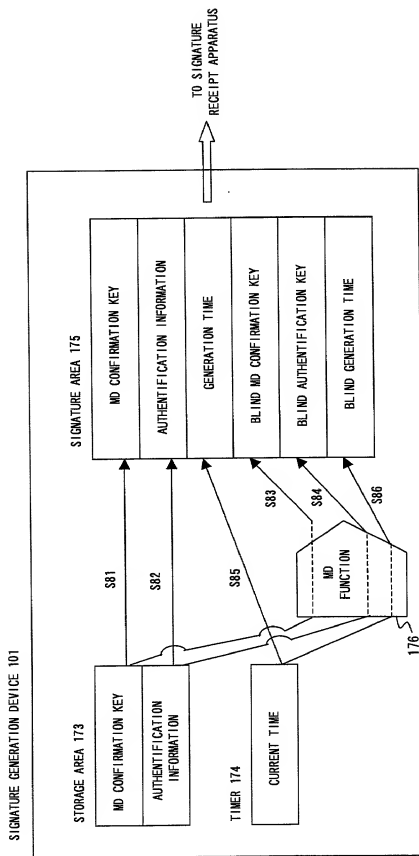
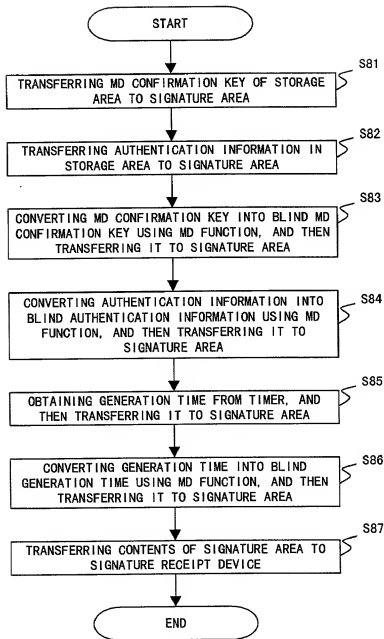


FIG. 21

[illegible]

F I G. 22

```

graph TD
    S91([START]) --> S92[RECEIVING DIGITAL RECEIPT SEAL]
    S92 --> S93{AUTHENTICATION INFORMATION OF  
DIGITAL RECEIPT SEAL MATCHES  
AUTHENTICATION INFORMATION  
STORED BY RECEIVER?}
    S93 -- YES --> S94[OBTAINING CURRENT TIME FROM  
TIMER]
    S94 --> S95{DIGITAL RECEIPT SEAL  
GENERATION TIME + ALLOWABLE  
TIME ≥ CURRENT TIME?}
    S95 -- YES --> S96[TRANSMITTING 'RECEIPT OK'  
NOTIFICATION TO GENERATING  
SIDE]
    S95 -- NO --> S97[TRANSMITTING 'RECEIPT  
ERROR' NOTIFICATION TO  
GENERATING SIDE]
    S93 -- NO --> S97
    S96 --> S98([END])
    S97 --> S98

```

F I G. 23

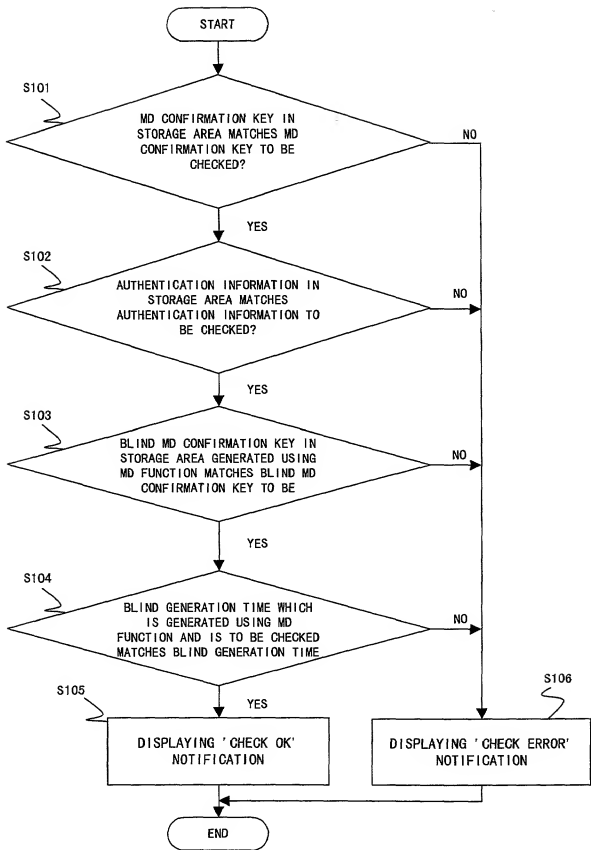


FIG. 24

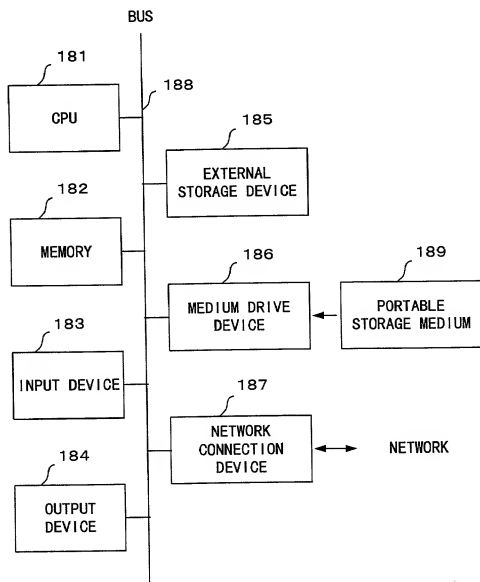


FIG. 25

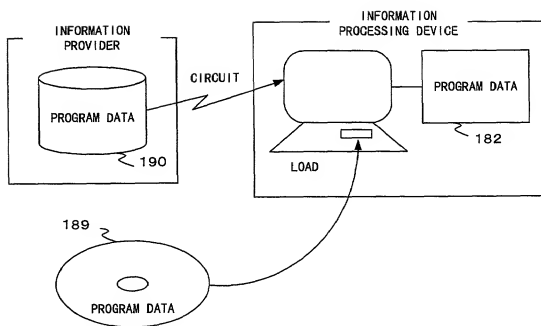


FIG. 26